

OSMANIA UNIVERSITY

M. E. (ECE) III – Semester AICTE (Main) Examination March-2021

TENTATIVE TIME – TABLE

No. 321/ME/Exams/2021

TIME: 10.00 AM TO 12:00 NOON

Date: 27-02-2021

DATE	DIGITAL SYSTEMS	EMBEDDED SYSTEMS & VLSI / EMBEDDED SYSTEMS & VLSI DESIGN	EMBEDDED SYSTEMS
16-03-2021	<ul style="list-style-type: none">• GNSS Signals & Receiver Technology• Speech Signal Processing• Image and Video Processing	<ul style="list-style-type: none">• Communication Buses & Interfaces• Field Programmable Gate Arrays	<ul style="list-style-type: none">• Energy Harvesting Technology & Power Management for IoT Devices
18-03-2021	<ul style="list-style-type: none">• Advanced Computer Networks• Wireless Channel Coding Techniques• Real Time Operating Systems	<ul style="list-style-type: none">• Advanced Digital Design with Verilog HDL• SoC Design	<ul style="list-style-type: none">• SoC Design• Real Time Operating Systems



S. V. Kumar
05/03/2021
CONTROLLER OF EXAMINATIONS