

# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

KUKATPALLY-HYDERABAD-500085

EXAMINATION BRANCH

**M.TECH-II SEMESTER -R09 REGULATION-SUPPLEMENTARY EXAMINATIONS FEBRUARY-2016**

**T I M E T A B L E**

**T I M E : 2.00 PM TO 5:00 PM**

COURSE	08-02-2016 MONDAY	10-02-2016 WEDNESDAY	12-02-2016 FRIDAY	15-02-2016 MONDAY	17-02-2016 WEDNESDAY	19-02-2016 FRIDAY	22-02-2016 MONDAY	24-02-2016 WEDNESDAY
BIOTECHNOLOGY-03	BIO REACTOR ENGINEERING	GENETIC ENGINEERING	DOWNSTREAM PROCESSING	ELECTIVE III/IV/V ENVIRONMENTAL BIO TECHNOLOGY	ELECTIVE III/IV/V ADVANCED IMMUNOLOGY AND IMMUNO TECHNOLOGY	ELECTIVE III/IV/V PLANT BIO TECHNOLOGY	ELECTIVE III/IV/V BIOINFORMATICS	ELECTIVE III/IV/V NANO BIO TECHNOLOGY AND NANO DEVICES ADVANCED ANIMAL CELL SCIENCE AND TECHNOLOGY
CAD/CAM-04	DESIGN OF HYDRAULICS AND PNEUMATICS SYSTEMS	FLEXIBLE MANUFACTURING SYSTEMS	COMPUTER AIDED MANUFACTURING	ELECTIVE III PRODUCTION AND OPERATIONS MANAGEMENT	ELECTIVE III COMPUTATIONAL FLUID DYNAMICS AUTOMATION IN MANUFACTURING	ELECTIVE IV COMPUTER AIDED PROCESS PLANNING INTELLIGENT MANUFACTURING SYSTEMS DESIGN OPTIMIZATION	INDUSTRIAL ROBOTICS	
COMPUTER SCIENCE-05	DISTRIBUTED COMPUTING	DISTRIBUTED DATABASES	ELECTIVE III WEB SERVICES INFORMATION RETRIEVAL SYSTEMS SEMANTIC WEB AND SOCIAL NETWORKS	ELECTIVE IV STORAGE AREA NETWORKS INFORMATION SECURITY WIRELESS NETWORKS AND MOBILE COMPUTING	ADVANCED COMPILER DESIGN	ADVANCED COMPUTER NETWORKS		
DIGITAL SYSTEM & COMPUTER ELECTRONICS-06	ADVANCED COMPUTER ARCHITECTURE	ELECTIVE III NETWORK SECURITY & CRYPTOGRAPHY ROBOTICS SYSTEM CHIP ARCHITECTURE	EMBEDDED REAL TIME OPERATING SYSTEMS	DESIGN OF FAULT TOLERANT SYSTEMS	LOW POWER VLSI DESIGN	ELECTIVE IV CMOS ANALOG & MIXED SIGNAL DESIGN ARTIFICIAL INTELLIGENCE DIGITAL SIGNAL PROCESSORS AND ARCHITECTURES		
ELECTRICAL POWER SYSTEM-07	ADVANCED POWER SYSTEM PROTECTION	HIGH VOLTAGE DC TRANSMISSION	POWER SYSTEM RELIABILITY	ELECTIVE III PROGRAMMABLE LOGIC CONTROLLERS AND THEIR APPLICATIONS ADVANCED DIGITAL SIGNAL PROCESSING EXTRA HIGH VOLTAGE TRANSMISSION	ELECTIVE IV EMBEDDED SYSTEMS POWER QUALITY NEURAL AND FUZZY SYSTEMS	POWER SYSTEM CONTROL AND STABILITY		
ENVIRONMENTAL ENGINEERING-11	AIR POLLUTION AND CONTROL	INDUSTRIAL WASTE WATER TREATMENT	SOLID WASTE MANAGEMENT	ENVIRONMENTAL IMPACT ASSESSMENT	ELECTIVE III GROUND WATER CONTAMINATION AND REMEDIATION DISASTER MANAGEMENT WATER QUALITY MODELING	ELECTIVE IV ENVIRONMENTAL BIO TECHNOLOGY REMOTE SENSING AND GIS ENVIRONMENTAL GEO TECHNOLOGY		

DATE:13-1-2016



CONTROLLER OF EXAMINATIONS

# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

KUKATPALLY-HYDERABAD-500085

EXAMINATION BRANCH

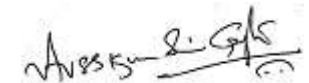
**M.TECH-II SEMESTER -R09 REGULATION-SUPPLEMENTARY EXAMINATIONS FEBRUARY-2016**

**T I M E T A B L E**

**T I M E : 2.00 PM TO 5:00 PM**

COURSE	08-02-2016 MONDAY	10-02-2016 WEDNESDAY	12-02-2016 FRIDAY	15-02-2016 MONDAY	17-02-2016 WEDNESDAY	19-02-2016 FRIDAY	22-02-2016 MONDAY	24-02-2016 WEDNESDAY
INDUSTRIAL ENGINEERING AND MANAGEMENT-13	SIMULATION MODELING OF MANUFACTURING SYSTEM	SUPPLY CHAIN MANAGEMENT	RELIABILITY ENGINEERING AND MAINTENANCE MANAGEMENT	PRODUCTION AND OPERATIONS MANAGEMENT	ELECTIVE III ENTERPRISE RESOURCE PLANNING TOTAL QUALITY MANAGEMENT	ELECTIVE IV MARKETING MANAGEMENT ENERGY MANAGEMENT		
MACHINE DESIGN-15	ADVANCED MECHANICS	MECHANICAL VIBRATIONS	ADVANCED OPTIMIZATION TECHNIQUES	EXPERIMENTAL STRESS ANALYSIS	ELECTIVE IV MECHATRONICS COMPUTATIONAL FLUID DYNAMICS THEORY OF PLATES AND SHELLS SIGNAL ANALYSIS AND CONDITION MONITORING	ELECTIVE III NON LINEAR VIBRATIONS DESIGN SYNTHESIS ROBOTICS PRESSURE VESSEL DESIGN		
PRODUCTION ENGINEERING-16	ADVANCED METAL FORMING	RAPID PROTOTYPING TECHNOLOGIES	OPTIMIZATION TECHNIQUES AND APPLICATIONS	AUTOMATION IN MANUFACTURING	ELECTIVE IV MECHATRONICS DESIGN FOR MANUFACTURE & ASSEMBLY INDUSTRIAL ROBOTICS	ELECTIVE III INTELLIGENT MANUFACTURING SYSTEMS FINITE ELEMENT TECHNIQUES DESIGN AND MANUFACTURE OF MEMS & MICROSYSTEMS		
STRUCTURAL ENGINEERING-20	FINITE ELEMENT METHODS	STRUCTURAL DYNAMICS	ANALYSIS AND DESIGN OF SHELLS AND FOLDED PLATES	ADVANCED STEEL DESIGN	ELECTIVE III COMPUTER AIDED DESIGN IN STRUCTURAL ENGINEERING (CAD) PRESTRESSED CONCRETE ADVANCED FOUNDATION ENGINEERING	ELECTIVE IV EARTHQUAKE RESISTANT DESIGN OF BUILDINGS STABILITY OF STRUCTURES PLASTIC ANALYSIS AND DESIGN PRINCIPLES OF BRIDGE ENGINEERING		
THERMAL ENGINEERING-21	FUELS COMBUSTION & ENVIRONMENT	ENERGY MANAGEMENT	FINITE ELEMENT ANALYSIS	COMPUTATIONAL FLUID DYNAMICS	ELECTIVE IV REFRIGERATION & AIRCONDITIONING THERMAL MEASUREMENTS AND PROCESS CONTROLS JET PROPULSION & ROCKETRY	ELECTIVE III EQUIPMENT DESIGN FOR THERMAL SYSTEMS THERMAL & NUCLEAR POWER PLANTS CONVECTIVE HEAT TRANSFER		

DATE:13-1-2016



CONTROLLER OF EXAMINATIONS

# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

KUKATPALLY-HYDERABAD-500085

EXAMINATION BRANCH

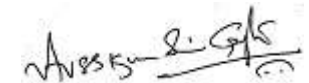
**M.TECH-II SEMESTER -R09 REGULATION-SUPPLEMENTARY EXAMINATIONS FEBRUARY-2016**

**T I M E T A B L E**

**T I M E : 2.00 PM TO 5:00 PM**

COURSE	08-02-2016 MONDAY	10-02-2016 WEDNESDAY	12-02-2016 FRIDAY	15-02-2016 MONDAY	17-02-2016 WEDNESDAY	19-02-2016 FRIDAY	22-02-2016 MONDAY	24-02-2016 WEDNESDAY
SOFTWARE ENGINEERING-25	DISTRIBUTED COMPUTING	ELECTIVE IV INTRODUCTION TO NEURAL NETWORKS SPEECH PROCESSING IMAGE PROCESSING AND PATTERN RECOGNITION	SOFTWARE ARCHITECTURE AND DESIGN PATTERNS	SERVICE ORIENTED ARCHITECTURE	SOFTWARE QUALITY ASSURANCE AND TESTING	ELECTIVE III MULTIMEDIA AND RICH INTERNET DEVELOPMENT SCRIPTING LANGUAGES SEMANTIC WEB AND SOCIAL NETWORKS		
MICRO WAVE AND RADAR ENGINEERING-30	MICROWAVE NETWORKS	MICROWAVE SOLID STATE DEVICES	ELECTIVE III OPTICAL COMMUNICATION TECHNOLOGY RF CIRCUIT DESIGN SOFTWARE RADIO	PHASED ARRAY SYSTEMS	RADAR SIGNAL PROCESSING	ELECTIVE IV MICRO CONTROLLERS FOR EMBEDDED SYSTEMS DESIGN SMART ANTENNAS WIRELESS COMMUNICATION AND NETWORKS		
ADVANCED MANUFACTURING SYSTEMS-33	SIMULATION MODELING OF MANUFACTURING SYSTEM	QUALITY ENGINEERING IN MANUFACTURING	COMPUTER AIDED MANUFACTURING	PRODUCTION AND OPERATION MANAGEMENT	ELECTIVE IV INTELLIGENT MANUFACTURING SYSTEMS COMPUTATIONAL FLUID DYNAMICS PERFORMANCE MODELING AND ANALYSIS OF MANUFACTURING SYSTEMS	ELECTIVE III DESIGN AND MANUFACTURING OF MEMS AND MICRO SYSTEMS TOOL DESIGN INDUSTRIAL ROBOTICS		
COMPUTER AND COMMUNICATION ENGINEERING-36	CODING THEORY AND TECHNIQUES	WIRELESS COMMUNICATIONS & NETWORKS	ELECTIVE IV TELECOMMUNICATION SWITCHING & NETWORKS OPTICAL COMMUNICATIONS TECHNOLOGY DATA BASE MANAGEMENT SYSTEMS	ADVANCED COMPUTER NETWORKS	ELECTIVE III ADVANCED COMPUTER ARCHITECTURE MOBILE COMPUTING TECHNOLOGIES OPTICAL NETWORKS	NETWORK SECURITY & CRYPTOGRAPHY		
CONTROL ENGINEERING-37	OPTIMAL CONTROL THEORY	NONLINEAR SYSTEMS	PROCESS DYNAMICS & CONTROL	COMPUTER AIDED VLSI DESIGN	ELECTIVE IV NEURAL NETWORKS & FUZZY SYSTEMS INTELLIGENT & KNOWLEDGE BASED SYSTEMS REAL TIME SYSTEMS	ELECTIVE III RELIABILITY ENGINEERING ADAPTIVE AND LEARNING CONTROL CONTROL SYSTEM DESIGN		

DATE:13-1-2016



CONTROLLER OF EXAMINATIONS

# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

KUKATPALLY-HYDERABAD-500085

EXAMINATION BRANCH

**M.TECH-II SEMESTER -R09 REGULATION-SUPPLEMENTARY EXAMINATIONS FEBRUARY-2016**

**T I M E T A B L E**

**T I M E : 2.00 PM TO 5:00 PM**

COURSE	08-02-2016 MONDAY	10-02-2016 WEDNESDAY	12-02-2016 FRIDAY	15-02-2016 MONDAY	17-02-2016 WEDNESDAY	19-02-2016 FRIDAY	22-02-2016 MONDAY	24-02-2016 WEDNESDAY
DIGITAL ELECTRONICS AND COMMUNICATION SYSTEMS/ENGG-38	CODING THEORY AND TECHNIQUES	WIRELESS COMMUNICATIONS & NETWORKS	OPTICAL COMMUNICATIONS TECHNOLOGY	IMAGE & VIDEO PROCESSING	ELECTIVE III OPTICAL NETWORKS LOW POWER VLSI DESIGN RADAR SIGNAL PROCESSING	ELECTIVE IV NETWORK SECURITY & CRYPTOGRAPHY DESIGN FOR TESTABILITY DIGITAL SIGNAL PROCESSORS AND ARCHITECTURES		
INFORMATION TECHNOLOGY-40	NETWORK PROGRAMMING	ADHOC AND SENSOR NETWORKS	WEB SERVICES	INFORMATION SECURITY	ELECTIVE IV DISTRIBUTED SYSTEMS SOFTWARE ARCHITECTURE & DESIGN PATTERNS SOFTWARE QUALITY ASSURANCE AND TESTING INTERWORKING WITH TCP / IP	ELECTIVE III MOBILE COMPUTING NETWORK MANAGEMENT SYSTEMS WIRELESS SECURITY ADVANCED COMPUTER ARCHITECTURE		
POWER AND INDUSTRIAL DRIVES-42	POWER ELECTRONIC CONTROL OF AC DRIVES	DYNAMICS OF ELECTRIC MACHINES	ELECTIVE IV ENTERPRISE RESOURCE PLANNING ADVANCED DIGITAL SIGNAL PROCESSING PROGRAMMABLE LOGIC CONTROLLERS AND THEIR APPLICATIONS	FLEXIBLE AC TRANSMISSION SYSTEMS (FACTS)	ELECTIVE III POWER QUALITY EMBEDDED SYSTEMS RELIABILITY ENGINEERING	HIGH VOLTAGE DC TRANSMISSION		
POWER ELECTRONICS-43	POWER ELECTRONIC CONTROL OF AC DRIVES	NEURAL AND FUZZY SYSTEMS	MICROPROCESSOR AND MICROCONTROLLER	FLEXIBLE AC TRANSMISSION SYSTEMS (FACTS)	ELECTIVE III POWER QUALITY ADVANCED DIGITAL SIGNAL PROCESSING DIGITAL CONTROL SYSTEMS	ELECTIVE IV RELIABILITY ENGINEERING ENTERPRISE RESOURCE PLANNING EMBEDDED SYSTEMS		
SYSTEMS & SIGNAL PROCESSING-45	CODING THEORY AND TECHNIQUES	ELECTIVE III WIRELESS COMMUNICATION & NETWORKS	ADAPTIVE SIGNAL PROCESSING	IMAGE AND VIDEO PROCESSING	SPEECH PROCESSING	ELECTIVE IV DIGITAL SIGNAL PROCESSORS AND ARCHITECTURES		
GEO-INFORMATICS & SURVEYING TECHNOLOGY-47	DIGITAL PHOTOGRAMETRY	GIS ANALYSIS AND APPLICATIONS	REMOTE SENSING APPLICATIONS	GNSS SURVEYING	GIS PROJECT PLANNING & MANAGEMENT	ELECTIVE II ADVANCED GEO SPATIAL TECHNOLOGY DIGITAL IMAGE PROCESSING - II REMOTE SENSING AND GIS FOR URBAN PLANNING AND MANAGEMENT REMOTE SENSING AND GIS FOR ENVIRONMENTAL ENGINEERING		

DATE:13-1-2016



CONTROLLER OF EXAMINATIONS

# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

KUKATPALLY-HYDERABAD-500085

EXAMINATION BRANCH

**M.TECH-II SEMESTER -R09 REGULATION-SUPPLEMENTARY EXAMINATIONS FEBRUARY-2016**

**T I M E T A B L E**

**T I M E : 2.00 PM TO 5:00 PM**

COURSE	08-02-2016 MONDAY	10-02-2016 WEDNESDAY	12-02-2016 FRIDAY	15-02-2016 MONDAY	17-02-2016 WEDNESDAY	19-02-2016 FRIDAY	22-02-2016 MONDAY	24-02-2016 WEDNESDAY
ELECTRICAL POWER ENGINEERING-49	ADVANCED POWER SYSTEM PROTECTION	NEURAL AND FUZZY SYSTEMS	ELECTIVE III DIGITAL CONTROL SYSTEMS DISTRIBUTION AUTOMATION DIGITAL SIGNAL PROCESSING	FLEXIBLE AC TRANSMISSION SYSTEMS	POWER QUALITY	ELECTIVE IV RELIABILITY ENGINEERING POWER SYSTEM OPERATION AND CONTROL ENTERPRISE RESOURCE PLANNING		
CHEMICAL ENGINEERING-51	ADVANCED SEPERATION PROCESSES	ADVANCED PROCESS CONTROL	ADVANCED MATHEMATICS IN CHEMICAL ENGINEERING	ELECTIVE III COMPUTATIONAL FLUID DYNAMICS INDUSTRIAL MICROBIAL PRODUCTS ADVANCED CHEMICAL ENGINEERING PLANT DESIGN	PROCESS SYSTEMS ENGINEERING APPLIED NUMERICAL METHODS OPTIMIZATION TECHNIQUES IN CHEMICAL ENGINEERING	ELECTIVE IV ENERGY CONSERVATION IN CHEMICAL PROCESS INDUSTRIES MEMBRANE TECHNOLOGY ENVIRONMENTAL IMPACT ASSESSMENT		
DESIGN FOR MANUFACTURING-52	DESIGN OF HYDRAULICS AND PNEUMATICS SYSTEMS	TOTAL QUALITY MANAGEMENT	COMPUTER AIDED MANUFACTURING	ELECTIVE III PRODUCTION AND OPERATION MANAGEMENT TOOL DESIGN INDUSTRIAL ROBOTICS	ELECTIVE IV COMPUTATIONAL FLUID DYNAMICS PERFORMANCE MODELING AND ANALYSIS OF MANUFACTURING SYSTEMS AUTOMATION IN MANUFACTURING	DESIGN AND MANUFACTURING OF MEMS AND MICRO SYSTEMS		
POWER SYSTEM CONTROL AND AUTOMATION-53	FLEXIBLE AC TRANSMISSION SYSTEMS	POWER SYSTEM CONTROL AND STABILITY	NEURAL AND FUZZY SYSTEMS	ELECTIVE II OPERATION RESEARCH EMBEDDED SYSTEMS POWER QUALITY	REAL TIME CONTROL OF POWER SYSTEMS	EXTRA HIGH VOLTAGE TRANSMISSION		
POWER ELECTRONICS AND ELECTRIC DRIVES-54	POWER ELECTRONIC CONTROL OF AC DRIVES	DYNAMICS OF ELECTRIC MACHINES	ELECTIVE IV PROGRAMMABLE LOGIC CONTROLLERS AND THEIR APPLICATIONS ADVANCED DIGITAL SIGNAL PROCESSING ENTERPRISE RESOURCE PLANNING	FLEXIBLE AC TRANSMISSION SYSTEMS (FACTS)	ELECTIVE III EMBEDDED SYSTEMS POWER QUALITY RELIABILITY ENGINEERING	HIGH VOLTAGE DC TRANSMISSION		
EMBEDDED SYSTEMS-55	CPLD & FPGA ARCHITECTURES AND APPLICATIONS	HARDWARE SOFTWARE CODESIGN	DIGITAL SIGNAL PROCESSORS AND ARCHITECTURES	SYSTEM MODELING AND SIMULATION ADVANCED COMPUTER NETWORKS(SUBSTITUTE THEORY)	ELECTIVE III LOW POWER VLSI DESIGN ADHOC WIRELESS & SENSOR NETWORKS ROBOTICS	ELECTIVE IV IMAGE & VIDEO PROCESSING NETWORK SECURITY & CRYPTOGRAPHY SYSTEM ON CHIP ARCHITECTURE		

DATE:13-1-2016



CONTROLLER OF EXAMINATIONS

# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

KUKATPALLY-HYDERABAD-500085

EXAMINATION BRANCH

**M.TECH-II SEMESTER -R09 REGULATION-SUPPLEMENTARY EXAMINATIONS FEBRUARY-2016**

**T I M E T A B L E**

**T I M E : 2.00 PM TO 5:00 PM**

COURSE	08-02-2016 MONDAY	10-02-2016 WEDNESDAY	12-02-2016 FRIDAY	15-02-2016 MONDAY	17-02-2016 WEDNESDAY	19-02-2016 FRIDAY	22-02-2016 MONDAY	24-02-2016 WEDNESDAY	
POWER SYSTEM (HIGH VOLTAGE)-56	ELECTIVE III POWER SYSTEM OPERATION AND CONTROL	PARTIAL DISCHARGE IN HIGH VOLTAGE EQUIPMENT	SURGE PHENOMENA AND INSULATION COORDINATION	ELECTIVE IV FLEXIBILITY AC TRANSMISSION SYSTEMS	POWER SYSTEM PROTECTION WITH STATIC RELAYS	HIGH VOLTAGE TESTING TECHNIQUES			
	POWER SYSTEM DYNAMICS AND STABILITY			COLLISION PHENOMENA AND PLASMA ELECTRONICS VOLTAGE STABILITY					
VLSI SYSTEM DESIGN / VLSI DESIGN-57	CMOS ANALOG & MIXED SIGNAL DESIGN	SYSTEM ON CHIP ARCHITECTURE	EMBEDDED REAL TIME OPERATING SYSTEMS	DESIGN OF FAULT TOLERANT SYSTEMS	ELECTIVE IV SYSTEM MODELING AND SIMULATION	ELECTIVE III DIGITAL SIGNAL PROCESSORS AND ARCHITECTURES			
					SEMICONDUCTOR MEMORY DESIGN & TESTING				IMAGE & VIDEO PROCESSING
					LOW POWER VLSI DESIGN				VLSI SIGNAL PROCESSING
COMPUTER SCIENCE ENGINEERING-58	DISTRIBUTED COMPUTING	DISTRIBUTED DATABASES	ELECTIVE III INFORMATION RETRIEVAL SYSTEMS	ELECTIVE IV WIRELESS NETWORKS AND MOBILE COMPUTING	ADVANCED COMPUTER ARCHITECTURE	ADVANCED COMPUTER NETWORKS			
			SEMANTIC WEB AND SOCIAL NETWORKS						STORAGE AREA NETWORKS
			WEB SERVICES						INFORMATION SECURITY
COMMUNICATION SYSTEMS-61	CODING THEORY AND TECHNIQUES	WIRELESS COMMUNICATIONS & NETWORKS	OPTICAL COMMUNICATIONS TECHNOLOGY	TELECOMMUNICATION SWITCHING & NETWORKS	ELECTIVE III OPTICAL NETWORKS	ELECTIVE IV PROPAGATION MODELS FOR WIRELESS COMMUNICATIONS			
					ADHOC WIRELESS & SENSOR NETWORKS				NETWORK SECURITY & CRYPTOGRAPHY
					MOBILE COMPUTING TECHNOLOGIES				SOFTWARE RADIO
WEB TECHNOLOGIES-62	SCRIPTING LANGUAGES	WEB SERVICES	ELECTIVE III MULTIMEDIA AND RICH INTERNET APPLICATIONS	GRID COMPUTING	ELECTIVE IV SOFTWARE PROCESS AND PROJECT MANAGEMENT	SEMANTIC WEB AND SOCIAL NETWORKS			
			CLOUD COMPUTING		SOFTWARE QUALITY ASSURANCE AND TESTING				
			WEB DATA MINING		SOFTWARE ARCHITECTURE & DESIGN PATTERNS				
IMAGE PROCESSING-63	NEURAL NETWORKS	REMOTE SENSING & GIS	ROBOTICS AND MACHINE VISION	ELECTIVE IV GENETIC ALGORITHMS	ELECTIVE III DATA MINING	PARALLEL ALGORITHMS			
				MULTIMEDIA DATABASES					BIOMETRICS
				STORAGE AREA NETWORKS					RADAR IMAGING

DATE:13-1-2016



CONTROLLER OF EXAMINATIONS

# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

KUKATPALLY-HYDERABAD-500085

EXAMINATION BRANCH

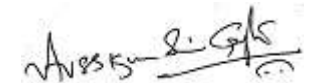
**M.TECH-II SEMESTER -R09 REGULATION-SUPPLEMENTARY EXAMINATIONS FEBRUARY-2016**

**TIMETABLE**

**TIME : 2.00 PM TO 5:00 PM**

COURSE	08-02-2016 MONDAY	10-02-2016 WEDNESDAY	12-02-2016 FRIDAY	15-02-2016 MONDAY	17-02-2016 WEDNESDAY	19-02-2016 FRIDAY	22-02-2016 MONDAY	24-02-2016 WEDNESDAY
POWER ENGINEERING AND ENERGY SYSTEMS-64	ADVANCED POWER SYSTEM PROTECTION	ELECTRICAL ENERGY CONVERSION	ELECTIVE III THERMAL POWER PLANTS HYDEL POWER ENGINEERING ENERGY MANAGEMENT AND THERMAL ENERGY CONVERSION	ELECTIVE IV FLEXIBLE AC TRANSMISSION DISTRIBUTION AUTOMATION EXTRA HIGH VOLTAGE TRANSMISSION	INSTRUMENTATION	RENEWABLE ENERGY RESOURCES		
WIRELESS AND MOBILE COMMUNICATION-65	CODING THEORY AND TECHNIQUES	WIRELESS COMMUNICATIONS & NETWORKS	OPTICAL COMMUNICATIONS TECHNOLOGY	TELECOMMUNICATION SWITCHING & NETWORKS	ELECTIVE III MOBILE COMPUTING TECHNOLOGIES ADHOC WIRELESS & SENSOR NETWORKS OPTICAL NETWORKS	ELECTIVE IV SOFTWARE RADIO PROPAGATION MODELS FOR WIRELESS COMMUNICATIONS NETWORK SECURITY & CRYPTOGRAPHY		
NANO TECHNOLOGY-66	MATERIAL CHARACTERIZATION TECHNIQUES	PROPERTIES OF NANO STRUCTURES	NANO ELECTRONICS AND NANO PHOTONICS	CARBON NANOTUBES AND APPLICATIONS	MEMS/NEMS DESIGN AND APPLICATION	NANO COMPOSITES - DESIGN AND SYNTHESIS		
VLSI AND EMBEDDED SYSTEMS-68	CMOS ANALOG & MIXED SIGNAL DESIGN	SYSTEM ON CHIP ARCHITECTURE	EMBEDDED REAL TIME OPERATING SYSTEMS	DESIGN OF FAULT TOLERANT SYSTEMS	ELECTIVE IV SYSTEM MODELING AND SIMULATION SEMICONDUCTOR MEMORY DESIGN & TESTING LOW POWER VLSI DESIGN	ELECTIVE III IMAGE & VIDEO PROCESSING VLSI SIGNAL PROCESSING DIGITAL SIGNAL PROCESSORS AND ARCHITECTURES		
ELECTRONICS AND COMMUNICATION ENGINEERING-70	CODING THEORY AND TECHNIQUES	WIRELESS COMMUNICATIONS & NETWORKS	OPTICAL COMMUNICATIONS TECHNOLOGY	IMAGE & VIDEO PROCESSING	ELECTIVE III LOW POWER VLSI DESIGN RADAR SIGNAL PROCESSING OPTICAL NETWORKS	ELECTIVE IV DIGITAL SIGNAL PROCESSORS AND ARCHITECTURES DESIGN FOR TESTABILITY NETWORK SECURITY & CRYPTOGRAPHY		
PARALLEL COMPUTING-74	DISTRIBUTED DATA BASES	ADHOC SENSOR NETWORKS	GRID COMPUTING	INFORMATION SECURITY	ELECTIVE III DATA MINING MACHINE LEARNING PATTERN RECOGNITION	ELECTIVE IV CLOUD COMPUTING PERSVASIVE COMPUTING DISTRIBUTED COMPUTING		
CONTROL SYSTEMS-75	OPTIMAL CONTROL THEORY	ADAPTIVE CONTROL THEORY	ELECTIVE IV ADVANCED DIGITAL SIGNAL PROCESSING OPERATIONS RESEARCH	ELECTIVE III ROBOTICS AND CONTROL DISTRIBUTED CONTROL SYSTEMS	NEURAL NETWORKS AND FUZZY SYSTEMS	CONTROL SYSTEM DESIGN		

DATE:13-1-2016



CONTROLLER OF EXAMINATIONS

# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

KUKATPALLY-HYDERABAD-500085

EXAMINATION BRANCH

**M.TECH-II SEMESTER -R09 REGULATION-SUPPLEMENTARY EXAMINATIONS FEBRUARY-2016**

**T I M E T A B L E**

**T I M E : 2.00 PM TO 5:00 PM**

COURSE	08-02-2016 MONDAY	10-02-2016 WEDNESDAY	12-02-2016 FRIDAY	15-02-2016 MONDAY	17-02-2016 WEDNESDAY	19-02-2016 FRIDAY	22-02-2016 MONDAY	24-02-2016 WEDNESDAY
AEROSPACE ENGINEERING-76	AIRCRAFT CONTROL AND SIMULATION	SPACE TRANSPORTATION SYSTEMS	COMPUTATIONAL APPROACHES TO AEROSPACE VEHICLE DESIGN	AEROSPACE SENSORS AND MEASUREMENT SYSTEMS	ELECTIVE III ADVANCED TOPICS IN AIR TRAFFIC MANAGEMENT SYSTEMS SPACECRAFT DYNAMICS AND CONTROL DYNAMICS AND CONTROL OF STRUCTURES AEROTHERMODYNAMICS OF HYPERSONIC FLIGHT MISSILE GUIDANCE	ELECTIVE IV OPTIMAL CONTROL HIGH ANGLE OF ATTACK AERODYNAMICS TACTICAL MISSILE DESIGN ROCKET AND SPACECRAFT PROPULSION MECHANICS OF COMPOSITE MATERIALS		
EMBEDDED SYSTEMS & VLSI DESIGN-77	CMOS ANALOG & MIXED SIGNAL DESIGN	SYSTEM ON CHIP ARCHITECTURE	EMBEDDED REAL TIME OPERATING SYSTEMS	DESIGN OF FAULT TOLERANT SYSTEMS	ELECTIVE IV SYSTEM MODELING AND SIMULATION LOW POWER VLSI DESIGN SEMICONDUCTOR MEMORY DESIGN & TESTING	ELECTIVE III DIGITAL SIGNAL PROCESSORS AND ARCHITECTURES IMAGE & VIDEO PROCESSING VLSI SIGNAL PROCESSING		
COMPUTER NETWORKS AND INFORMATION SECURITY-78	WIRELESS NETWORKS	INFORMATION SECURITY II	ELECTIVE IV ADHOC AND SENSOR NETWORKS WIRELESS SECURITY NETWORK MANAGEMENT AND PERFORMANCE EVALUATION INFORMATION RETRIEVAL SYSTEMS	INFORMATION SECURITY MANAGEMENT AND STANDARDS	MOBILE COMPUTING	ELECTIVE III DISTRIBUTED SYSTEMS COMMUNICATION PROTOCOL ENGINEERING DATABASE SECURITY WEB SERVICES		
MECHATRONICS-80	MICRO CONTROLLER & APPLICATIONS	FUZZY LOGIC & NEURAL NETWORKS	MEMS	INDUSTRIAL ROBOTICS	DESIGN FOR MANUFACTURE & ASSEMBLY INTELLIGENT MANUFACTURING SYSTEMS SIMULATION MODELING OF MANUFACTURING SYSTEMS	AUTOTRONICS & VEHICLE INTELLIGENCE COMPUTER AIDED METROLOGY & MACHINEVISION ENTREPRENEURSHIP		
COMPUTER NETWORKS-82	WIRELESS NETWORKS	ELECTIVE IV ECOMMERCE	ELECTIVE III INFORMATION RETRIEVAL SYSTEMS	INTERNET NETWORKING WITH TCP/IP	MOBILE COMPUTING	DISTRIBUTED SYSTEMS		
REAL TIME SYSTEMS-84	REAL TIME SYSTEMS PROGRAMMING LANGUAGES	EMBEDDED SYSTEMS DEVELOPMENT	REAL TIME OPERATING SYSTEMS	ELECTIVE IV CASE STUDIES IN REAL TIME APPLICATIONS NEURAL NETWORKS MOBILE COMPUTING	ELECTIVE III CASE STUDIES IN REAL TIME OPERATING SYSTEMS PARALLEL ALGORITHMS LOW POWER VLSI DESIGN	QUALITY ASSURANCE OF REAL TIME SYSTEMS		

DATE:13-1-2016



CONTROLLER OF EXAMINATIONS



# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

KUKATPALLY-HYDERABAD-500085

EXAMINATION BRANCH

**M.TECH-II SEMESTER -R09 REGULATION-SUPPLEMENTARY EXAMINATIONS FEBRUARY-2016**

**TIMETABLE**

**TIME : 2.00 PM TO 5:00 PM**

COURSE	08-02-2016 MONDAY	10-02-2016 WEDNESDAY	12-02-2016 FRIDAY	15-02-2016 MONDAY	17-02-2016 WEDNESDAY	19-02-2016 FRIDAY	22-02-2016 MONDAY	24-02-2016 WEDNESDAY
HIGHWAY ENGINEERING-87	HIGHWAY PROJECT FORMULATION & ECONOMICS	PAVEMENT CONSTRUCTION MAINTENANCE & MANAGEMENT	TRANSPORTATION SYSTEM MANAGEMENT	TRAFFIC ANALYSIS	ELECTIVE III ENVIRONMENTAL IMPACT ASSESSMENT FOR TRANSPORTATION PROJECTS ADVANCED MODELING TECHNIQUES IN HIGHWAY ENGINEERING LAND USE AND TRANSPORTATION MODELING	ELECTIVE IV ADVANCED TRAVEL DEMAND MODELLING GIS APPLICATIONS IN TRANSPORTATION ENGINEERING OPTIMIZATION TECHNIQUES		
ELECTRONICS & INSTRUMENTATION-88	DATA ACQUISITION SYSTEM	QUALITY & RELIABILITY ENGINEERING	PC BASED INSTRUMENTATION	VIRTUAL INSTRUMENTATION	INDUSTRIAL INSTRUMENTATION	FIBER OPTIC AND LASER BASED INSTRUMENTATION		
HEATING VENTILATION & AIR CONDITIONING-91	ADVANCED HEAT AND MASS TRANSFER	AIRCONDITIONING II	ADVANCED INSTRUMENTATION CONTROL SYSTEMS	REFRIGERATION & A/C EQUIPMENTS & CONTROL	ELECTIVE III ENERGY MANAGEMENT EQUIPMENT DESIGN FOR THERMAL SYSTEMS FINITE ELEMENT ANALYSIS	ELECTIVE IV THERMAL TURBO MACHINES THERMAL AND NUCLEAR POWER PLANTS CRYOGENIC ENGINEERING		
SYSTEMS AND SIGNAL PROCESSING-93	CODING THEORY & TECHNIQUES	ELECTIVE IV SYSTEM ON CHIP ARCHITECTURE DIGITAL CONTROL SYSTEMS MOBILE COMPUTING TECHNOLOGIES	ELECTIVE III EMBEDDED REALTIME OPERATING SYSTEMS ADHOC WIRELESS AND SENSOR NETWORKS CPLD & FPGA ARCHITECTURES AND APPLICATION	ADAPTIVE SIGNAL PROCESSING	SPEECH PROCESSING	DSP PROCESSORS AND ARCHITECTURES		
ENGINEERING DESIGN-95	ADVANCED MACHINE DESIGN	INDUSTRIAL ROBOTICS	FINITE ELEMENT AND BOUNDARY ELEMENT METHODS	DIGITAL IMAGE PROCESSING AND COMPUTER VISION	ELECTIVE III MATERIALS HANDLING EQUIPMENT	ELECTIVE IV DESIGN FOR MANUFACTURING AND ASSEMBLY		
POWERPLANT ENGINEERING AND ENERGY MANAGEMENT-99	ADVANCED POWER PLANT ENGINEERING	POWER PLANT INSTRUMENTATION	POWER PLANT MAINTENANCE	ENERGY CONSERVATION	EQUIPMENT DESIGN FOR THERMAL SYSTEMS	ELECTIVE II POWER PLANT PROTECTION AND SWITCHGEAR ENVIRONMENT&SAFETY ENGINEERING POWER DISTRIBUTION SYSTEM POWER PLANT PERFORMANCE MONITORING&TESTING		

DATE:13-1-2016

NOTE:



CONTROLLER OF EXAMINATIONS

**(i). ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE CONTROLLER OF EXAMINATIONS IMMEDIATELY  
(ii). EVEN IF GOVERNMENT DECLARES HOLIDAY ON ANY OF THE ABOVE DATES.THE EXAMINATIONS SHALL BE CONDUCTED AS USUAL**

EngineersHub